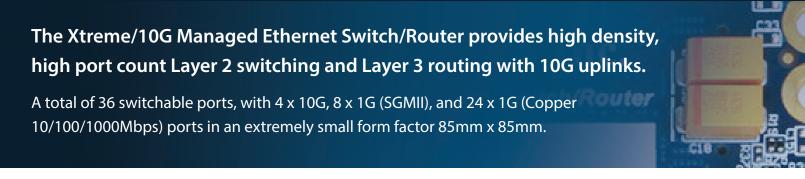


XTREME/10G MANAGED ETHERNET SWITCH/ROUTER

PART NUMBERS: XDG201, XDG201-01



The device targets managed Layer 2 and Layer 3 equipment in SMB, SME, and industrial applications where high port count 1G switching with 10G aggregation/uplinks are required.



HARDWARE SPECIFICATIONS

Ethernet Switch Engine	Vitesse VSC7448 Carrier Grade Ethernet Switch Chipset	I/O Connectors	High-Density 440-pin Board-to-Board Connector
Ports	36 Total Switching Ports 4x 10G (SFI) Ports 8x 1G (SGMII) Ports 24x 1G (Copper 10/100/1000Mbps) Ports	Memories	 4Gb DDR3 SDRAM 128Mb Serial NOR Flash
Layer 2 & Layer 3 Forwarding	 IEEE802.1Q switch with 4K VLANs and 32K MAC table entries Push/pop up to 3VLAN tags on ingress and egress RSTP and MSTP support Fully non-blocking wire-speed switching performance for all frame sizes IPv4/IPv6 unicast and multicast Layer 2 switching with up to 32K groups and 1K port masks IPv4/IPv6 unicast and multicast Layer 3 forwarding (routing) with reverse path forwarding (RPF) support IGMPv2, IGMPv3, MLDv1, and MLDv2 support Link aggregation (IEEE 802.3ad) Independent and shared VLAN learning (IVL, SVL) Jumbo frame support 	Quality of Service	 Four megabytes of integrated shared packet memory Eight QoS classes with a pool of up to 32K queues TCAM-based classification with pat tern matching against Layer 2 through Layer 4 information Dual- rate policers selected by VCAP IS2, eight single- rate priority policers per port, and four single-rate port policers for each port Flexible 4K ingress QoS mappings and 8K egress QoS mappings for VLAN tags and DSCP values Up to 4K egress VLAN tag operations Audio/ video bridging (AVB) with support for time- synchronized, low- latency, and video streaming services Priority-based f low control (PFC) (IEEE 802.1Qbb)
Security	 Vitesse Content Aware Processor (VCAP[™]) packet filtering engine using ACLs for ingress and egress packet inspection Storm controllers for flooded broadcast, flooded multicast, and- flooded unicast traffic Per-port, per-address registration for copying/redirecting/ discarding 32 VCAP single-rate policers 	Layer 2 Switching Parameters	 Packet Buffer: 32Mb MAC Table Size: 32k Layer 2 Multicast Port Masks: 1k Super VCAP blocks: 8 VCAP CLM entries: 4k VCAP LPM entries: 4k/1k (IPv4/IPv6) VCAP IS2 entries: 4k/1k (IPv4/IPv6)
Maximum Bandwidth	80Gbps	Input Voltage	+4V to 14V Input Range (External Connector



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XTREME/10G MANAGED ETHERNET SWITCH/ROUTER

Ich/Router



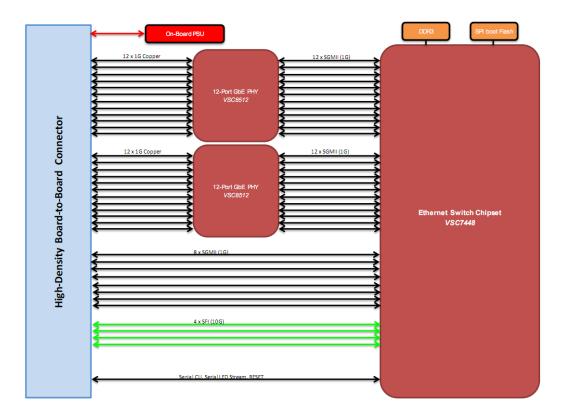
HARDWARE SPECIFICATIONS

Layer 3 Switching Parameters	 Router Legs: 128 IP unicast routes/hosts: 4k/1k (IPv4/IPv6) Next -hop/ ARP table entries: 2k IP multicast groups: 2k/512 Multicast router leg masks: 1k ECMPs: 16 	Management Access	 Web Interface CLI via RS-232 Software API SMNP
Operating Temperature	-40°C to +85°C (chipset rated to +125°C thermals) (-40°F to +185°F)	Warranty and Support	Limited One-Year Warranty and Free Technical Support



XDG201-01 - XDG201 Switch with XHG201 Heatplate installed Also Shown: XBG301 Development Breakout Board (Sold separately)

XBG301 SPECIFICATIONS				
Dimensions	125.84mm x 167.07mm x 49.10mm (4.95" x 6.58" x 1.93") (When XDG201-01 + XBG301 are integrated)			
Connects to XDG301 to provide	 4x 10G SFP+ Uplinks 4x 1G SFP Ports 24x Copper GbE Ports RS232 Console port 			
Power Input	+5V to +14V DC			
Custom Design Available				





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